



CITAES – 2018



First International Conference on Computational and Intelligent Techniques for Automation of Engineering Systems (CITAES)

Program Schedule

November 30 – December 1, 2018

Organized by

Department of Electrical and Electronics Engineering
Department of Electronics and Communication Engineering

**GUDLAVALLERU ENGINEERING COLLEGE
(AUTONOMOUS)**

Seshadri Rao Knowledge Village, Gudlavalleru – 521356
Krishna District, Andhra Pradesh, India

30th November 2018 (Friday) CITAES 2018

9.00 AM - 10.00 AM		REGISTRATION (Venue: Reception Desk)
10.00 AM - 11.00AM		INAUGURATION OF CITAES-2018 (Venue: Central Lecture Theater) Chief Guest: Dr. S. Srinivas Kumar, Vice Chancellor JNTUA, Anantapuramu, Guests of Honor: Dr. Ho Chiung Ching, Dr. Chattopadhyay Adhir B
11.00 AM - 12.15 PM		KEYNOTE ADDRESS BY (Venue: Central Lecture Theater) Dr. Ho Chiung Ching, Dean Faculty of Computing and Informatics, Multimedia University, Malaysia
12.15 PM - 12.30 PM		INTERACTION WITH Dr. Ho Chiung Ching, Dean Faculty of Computing and Informatics, Multimedia University, Malaysia Dr. Chattopadhyay Adhir Baran, Professor, BITS Dubai
12.30 PM - 1.30 PM		LUNCH BREAK

TECHNICAL SESSIONS

30/11/2018

		TRACK - 4 &5 Venue 1: Microsoft Innovation Centre Room No: M307		TRACK - 6&7 Venue 2: DECS Lab Room No: M321A		TRACK - 2&3 Venue 3: EEE AC Seminar Hall Room No: M222	
		Signal Processing & Communications Session Chair: 1. Dr. G. Mallikharjuna Rao, Scientist-F, RCI DRDO, Hyderabad 2. Dr. Y. Ramakrishna, Professor, ECE, GEC		VLSI-ES &Software Engineering Session Chair: 1. Dr. P. V. Rao, Professor, VBIT, Hyderabad 2. Dr. Ch. Balaswamy, Professor, GEC 3. Dr. M. Kamaraju, Professor, Mentor(AS&A), ECE, GEC		Power Electronics, Control & Automation Session Chair: 1. Dr. D. M. Vinod Kumar Professor, EE, NIT Warangal 2. Dr. K. Alice Mary, Professor, EEE, GEC	
1.30 PM - 2.00 PM		TALK BY SESSION CHAIR: Dr. G. Mallikharjuna Rao				TALK BY SESSION CHAIR: Dr. D. M. Vinod Kumar	

2.00 PM - 3.30 PM	Session-1A Organizing Committee: 1. Mr. T.S.R.Krishna Prasad, 2. Mr. M. V. Srikanth	CITAES2018_PAPER_4, CITAES2018_PAPER_54 CITAES2018_PAPER_8, CITAES2018_PAPER_67 CITAES2018_PAPER_14, CITAES2018_PAPER_77 CITAES2018_PAPER_31, CITAES2018_PAPER_96	Session-2A Organizing Committee: 1. Dr. Y. Syamala 2. Mr. B. Vamsi Krishna	CITAES2018_PAPER_3, CITAES2018_PAPER_53 CITAES2018_PAPER_29, CITAES2018_PAPER_58 CITAES2018_PAPER_48, CITAES2018_PAPER_81 CITAES2018_PAPER_51, CITAES2018_PAPER_115	Session-3A Organizing Committee: 1. Dr. G. Kishor Babu 2. Ms. Sk. Jahera	CITAES2018_PAPER_13, CITAES2018_PAPER_25 CITAES2018_PAPER_20, CITAES2018_PAPER_36 CITAES2018_PAPER_47, CITAES2018_PAPER_15 CITAES2018_PAPER_205 CITAES2018_PAPER_41	
3.30 PM - 3.45 PM		TEA BREAK		TEA BREAK		TEA BREAK	
3.45 PM - 4.45 PM	Session-1B Organizing Committee: 1. Mr. T.S.R.Krishna Prasad, 2. Mr. M. V. Srikanth	CITAES2018_PAPER_101, CITAES2018_PAPER_416 CITAES2018_PAPER_104, CITAES2018_PAPER_420 CITAES2018_PAPER_427 CITAES2018_PAPER_434	Session-2B Organizing Committee: 1. Dr. Y. Syamala 2. Mr. B. Vamsi Krishna	CITAES2018_PAPER_26, CITAES2018_PAPER_60 CITAES2018_PAPER_56, CITAES2018_PAPER_61 CITAES2018_PAPER_423, CITAES2018_PAPER_106 CITAES2018_PAPER_435	Session-3B Organizing Committee: 1. Dr. G. Kishor Babu 2. Ms. Sk. Jahera	CITAES2018_PAPER_59, CITAES2018_PAPER_86 CITAES2018_PAPER_83, CITAES2018_PAPER_87 CITAES2018_PAPER_408, CITAES2018_PAPER_76 CITAES2018_PAPER_206	

1st December 2018 (Saturday) CITAES 2018

9.30 AM - 10.00 AM		INVITED TALK BY (Venue: Central Lecutre Theater) Dr. Chattopadhyay Adhir Baran, Professor, BITS, Dubai					
TECHNICAL SESSIONS							
01/12/2018							
		TRACK - 4 &5 Venue 1: Microsoft Innovation Centre Room No: M307		TRACK - 6&7 Venue 2: DECS Lab Room No: M321A		TRACK-1 Venue 3: EEE AC Seminar Hall Room No: M222	

		<p>Signal Processing & Communications</p> <p>Session Chair:</p> <p>1. Ramasamy S R T, Vice President, Efftronics Systems Pvt. Ltd. Vijayawada</p> <p>2. Dr.V. V. K. D. V. Prasad Professor & HoD, ECE, GEC</p> <p>3. Dr. R. Srinivasa Rao, Professor, ECE, GEC</p>		<p>VLSI-ES &Software Engineering</p> <p>Session Chair:</p> <p>1. Dr. P. Sri Hari Rao, Associate Professor, ECE, NIT Warangal</p> <p>2. Dr. D. Ravi Krishna Reddy, Professor, ECE, GEC</p>		<p>Power Systems</p> <p>Session Chair</p> <p>1. Dr. D. V. S. S. Siva Sarma, Professor, EE, NIT Warangal</p> <p>2. Dr. G. Srinivasa Rao, Professror, EEE, GEC</p>	
10.00 AM- 10.30 AM		<p>TALK BY SESSION CHAIR(s): 1. Ramasamy S R T /</p> <p>2. Dr. P. Sri Hari Rao</p>				<p>TALK BY SESSION CHAIR:</p> <p>Dr. D. V. S. S. Siva Sarma</p>	
10.30 AM -11.30 AM	<p>Session-1C</p> <p>Organizing Committee:</p> <p>1. Mr. A. Mallaiiah,</p> <p>2. Mrs. V. Priyadarshini</p>	<p>CITAES2018_PAPER_73, CITAES2018_PAPER_114</p> <p>CITAES2018_PAPER_107, CITAES2018_PAPER_411</p> <p>CITAES2018_PAPER_404, CITAES2018_PAPER_405</p>	<p>Session-2C</p> <p>Organizing Committee:</p> <p>1. Mrs. K. SreeLakshmi</p> <p>2. Mr. P. Ravi Shankar</p>	<p>CITAES2018_PAPER_27, CITAES2018_PAPER_39</p> <p>CITAES2018_PAPER_28, CITAES2018_PAPER_72</p> <p>CITAES2018_PAPER_406, CITAES2018_PAPER_407</p>	<p>Session-4A</p> <p>Organizing Committee:</p> <p>1. Mrs. J. L. Santha Kumari</p> <p>2. Mr. V. Naga Babu</p>	10.00 AM - 11.30 AM	<p>CITAES2018_PAPER_21, CITAES2018_PAPER_69</p> <p>CITAES2018_PAPER_23, CITAES2018_PAPER_78</p> <p>CITAES2018_PAPER_45, CITAES2018_PAPER_84</p> <p>CITAES2018_PAPER_50</p>
11.30 AM -11.45 AM		<p>TEA BREAK</p>		<p>TEA BREAK</p>		<p>TEA BREAK</p>	
11.45 AM - 1.00 PM	<p>Session-1D</p> <p>Organizing Committee:</p> <p>1. Mr. A. Mallaiiah,</p> <p>2. Mrs. V. Priyadarshini</p>	<p>CITAES2018_PAPER_412, CITAES2018_PAPER_415</p> <p>CITAES2018_PAPER_413, CITAES2018_PAPER_422</p> <p>CITAES2018_PAPER_64, CITAES2018_PAPER_428</p> <p>CITAES2018_PAPER_63 CITAES2018_PAPER_98</p>	<p>Session-2D</p> <p>Organizing Committee:</p> <p>1. Mrs. K. SreeLakshmi</p> <p>2. Mr. P. Ravi Shankar</p>	<p>CITAES2018_PAPER_95, CITAES2018_PAPER_431</p> <p>CITAES2018_PAPER_79, CITAES2018_PAPER_99</p> <p>CITAES2018_PAPER_100, CITAES2018_PAPER_414</p> <p>CITAES2018_PAPER_52, CITAES2018_PAPER_102</p>	<p>Session-4B</p> <p>Organizing Committee:</p> <p>1. Mrs. J. L. Santha Kumari</p> <p>2. Mr. V. Naga Babu</p>	11.45 AM - 1.00 PM	<p>CITAES2018_PAPER_90, CITAES2018_PAPER_94</p> <p>CITAES2018_PAPER_88, CITAES2018_PAPER_97</p> <p>CITAES2018_PAPER_110, CITAES2018_PAPER_204</p>
1.00 PM - 2.00 PM		<p>LUNCH BREAK</p>					

2.00 PM - 3.30 PM		<p>Signal Processing & Communications</p> <p>Session Chair:</p> <p>1. Dr. T. Kishore Kumar Professor, ECE, NIT Warangal</p> <p>2. Dr. R. Srinivasa Rao, Professor, ECE, GEC</p>		<p>VLSI-ES & Software Engineering</p> <p>Session Chair:</p> <p>1. Dr. P. Sri Hari Rao, Associate Professor, ECE, NIT Warangal</p> <p>2. Dr. D. Ravi Krishna Reddy, Professor, ECE, GEC</p>			
	<p>Session-1E</p> <p>Organizing Committee:</p> <p>1. Mr. Ch. Rambabu</p> <p>2. Vargil Vijay E</p>	<p>CITAES2018_PAPER_46, CITAES2018_PAPER_426</p> <p>CITAES2018_PAPER_424, CITAES2018_PAPER_430</p> <p>CITAES2018_PAPER_89, CITAES2018_PAPER_40</p> <p>CITAES2018_PAPER_432 CITAES2018_PAPER_433</p>	<p>Session-2E</p> <p>Organizing Committee:</p> <p>1. Mr. V. Vittal Reddy</p> <p>2. Mr. G. Ramakrishna</p>	<p>CITAES2018_PAPER_105, CITAES2018_PAPER_418</p> <p>CITAES2018_PAPER_108, CITAES2018_PAPER_425</p> <p>CITAES2018_PAPER_112, CITAES2018_PAPER_429</p> <p>CITAES2018_PAPER_417, CITAES2018_PAPER_401</p>			
3.30 PM - 3.45 PM		TEA BREAK		TEA BREAK			
3.45 PM - 4.45 PM		VALEDICTORY FUNCTION					

TACK 4 & 5

30-11-2018

SESSION - 1A : SIGNAL PROCESSING & COMMUNICATIONS

VENUE: MICROSOFT INNOVATION CENTER

ROOM NO: M307

TIME: 2.00 PM - 3.30 PM

S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_4	ECE	An efficient method for segmentation of non iris images using optimization based reconstructed FCM	Rapaka Satish, Pullakura Rajesh Kumar
2	CITAES2018_paper_8	ECE/CSE	EPA- SafeSec: Robust Privacy and Protection based Light Weight Cryptography	P. Raveendra Babu, Dr. Ch. Rupa, Dr. D. Naga Raju
3	CITAES2018_paper_14	ECE	Design of Gregorian Dual Reflector in Ku-Band and W-Band for Limited Scanning	Venkata Reddy Kandregula, Venkateswara Rao Tumati
4	CITAES2018_paper_31	CSE/CSE	Pen Test based Network Security Assessment and Protection	Bhargavi Kanuri, Dr. Ch. Rupa
5	CITAES2018_paper_54	ECE	Low redundancy matrix code with efficient no of PPAs	Bujjibabu Penumutchi, Sarvani Voruganti
6	CITAES2018_paper_67	ECE	Design and Implementation of an efficient IIR Filter Architectures Using Merged Delay Transformation	P Bujjibabu, Dr.M Kamaraju, Dr.K Babulu,
7	CITAES2018_paper_77	ECE	A Novel Method of Image Fusion Using Guided Image Filter and Image Statistics	M.V.Srikanth, Dr.V.V.K.D.V.Prasad Dr.K.Satyaprasad
8	CITAES2018_paper_96	CSE	UAV based waste monitoring using Convolutional neural networks	P.Naga Sravanthi, P.Ramya, M. Mahesh Kumar

TACK 4 & 5

30-11-2018

SESSION - 1B : SIGNAL PROCESSING & COMMUNICATIONS

VENUE: MICROSOFT INNOVATION CENTER

ROOM NO: M307

TIME: 3.45 PM - 4.45 PM

S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_101	CSE	Real-time Face Recognition under dim light condition and self- occlusion for indoor Safety	Roshni Singh, Dr. Leena R. Ragma
2	CITAES2018_paper_104	ECE	COMPARATIVE ANALYSIS FOR LEAD FREE PIEZOELECTRIC MATERIAL FOR ULTRASONIC GLUCOSE SENSING APPLICATIONS	JYOTHIRMAI NARINA, MIRANJI KATTA
3	CITAES2018_paper_416	ECE	IMPLEMENTATION OF RECTANGULAR SPIRAL PATCH ANTENNA BY USING COAXIAL FEED	E. Vargil Vijay, Dr.D.Prabhakar
4	CITAES2018_paper_420	ECE	A survey of energy efficient optimization algorithms in Wireless Sensor Networks	Ch.Rambabu, Dr.V.V.K.D.V.Prasad, Dr.K.Satya Prasad
5	CITAES2018_paper_427	ECE	Robust Speaker Recognition For Large -Scale Data Using Silence Removal Techniques	Dr Rama Koteswara Rao P, Dr D Ratna Kishore, Abdul Rehman Shaik
6	CITAES2018_PAPER_434	ECE	Tri band CP slot antenna backed with Dual FSS reflector	B.Siva Hari Prasad, Dr. M.V.S. Prasad,

TRACK 6 & 7				
30-11-2018 SESSION - 2A : VLSI-ES & SOFTWARE ENGINEERING				
VENUE: DECS LAB ROOM NO: M321A TIME: 2.00 PM - 3.30 PM				
S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_3	ECE	Novel strategies of low power Subthreshold SRAM Designs under 32nm for real-time applications.	T. Vasudeva Reddy , K. Madhava Rao
2	CITAES2018_paper_29	CSE/CSE	Foreground Moving Object Detection Using Support Vector Machine (SVM)	M. Nagaraju, Dr. M. Baburao
3	CITAES2018_paper_48	ECE/CSE	RS BASED MULTIMEDIA CONCEALED DATA DETECTION	Sumaiya.Sk, Dr. Ch. Rupa
4	CITAES2018_paper_51	CSE	A Review on the Strategic Variants of Artificial Bee Colony (ABC) Algorithm for Optimization Problems	M Rao Batchanaboyina, D Naga Raju
5	CITAES2018_paper_53	ECE	Agricultural Crop Monitoring and Controlling Using IoT	V.Vineela, P.Bhavya, B.Sandeep
6	CITAES2018_paper_58	CSE	DISTRIBUTED WEB CONTENT MANAGEMENT SYSTEMS FOR E-LEARNING APPLICATIONS- A RESEARCH STUDY	MD ABDUL WASSAY , SURESH DARA
7	CITAES2018_paper_81	CSE	Reversible Data Concealing In Encrypted Images Using Binary- Square Embedding	Narendrababu Thumati, Dr.Kavitha Chaduvula
8	CITAES2018_paper_115	ECE	REVERSIBLE VEDIC MULTIPLIER	G. Venkata Latha, Y.Syamala, T. Anil Chowdary, G. Murali Krishna

TRACK 6 & 7

30-11-2018

SESSION - 2B : VLSI-ES & SOFTWARE ENGINEERING

VENUE: DECS LAB

ROOM NO: M321A

TIME: 3.45 PM - 4.45 PM

S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_26	ECE	A Novel ALU design using PNS-FCR: Static CMOS Logic for Microprocessors	T.Subhashini, Dr. M.Kamaraju , Dr. K.Babulu
2	CITAES2018_paper_56	ECE	FPGA based optimized Reconfigurable Base-2 constant coefficient multiplier architecture for Image Filtering	N.Sambamurthy, Dr. M.Kamaraju
3	CITAES2018_paper_60	CSE	Enhanced Learning Management System with Semantic Technologies and Inferences	MD ABDUL WASSAY , SURESH DARA
4	CITAES2018_paper_61	CSE	Proactive Co-Leader Nomination in Platoon Based VANETs	Rekha Gupta, Puja Padiya, Amarsinh Vidhate
5	CITAES2018_paper_106	ECE	DESIGN OF A NEMS CANTILEVER SENSOR FOR EXPLOSIVE DETECTION	LOKESH KUMAR P.V.T, Dr. TEJO MURTHY P.H.S
6	CITAES2018_paper_423	CSE	Prediction of customer churn in telecom sector using clustering technique	Vallabhaneni Renuka Devi, Mrs. G. Bharathi , Dr. G.V.S.N.R.V. Prasad
7	CITAES2018_PAPER_435	IT	SMART SYSTEM TO DETECT THE MATURITY LEVEL OF FRUIT WITH ITS SIZE USING IOT	B. Srinivasukumar, Dr.S.G. Santhi, Dr. S. Narayana

TRACK 2 & 3				
30-11-2018 SESSION - 3A : POWER ELECTRONICS, CONTROL & AUTOMATION				
VENUE: EEE AC SEMINAR HALL ROOM NO: M222 TIME: 2.00 PM - 3.30 PM				
S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_13	EEE	Optimal Tuning of PID Controller for Autonomous Hybrid Power System using Swarm Intelligence Techniques	V.S.R.Pavan Kumar.Neeli, Dr.U.Salma
2	CITAES2018_paper_15	EEE	PWM based Sliding Mode Controller for High Switching Frequency DC-DC Converter with State Space Averaged Modelling	J Ravindra, M Sushama
3	CITAES2018_paper_20	EEE	An improved Routh-Padé approximation of high-order continuous interval systems	D. P. S. Chauhan
4	CITAES2018_paper_25	EEE	Project Stratagems for a Nonlinear Vector Controller Fed BLDC Motor	T.V. Subhashini, Dr. K. Alice Mary,
5	CITAES2018_paper_36	EEE	Speed control of pmsm with high step up dual switch mppt solar converter	Pramod Gollapelli, Venu Madhav Gopala
6	CITAES2018_paper_47	EEE	Comparative Study of Induction Generator And Synchronous Generator Using D-Q Model	Vemireddi Tejeswara Rao
7	CITAES2018_paper_205	EEE	A voltage controlled D-STATCOM based voltage regulator for low voltage distribution grids	Dr. G. Kishor Babu, Mr. Rajesh V, Dr. B. Madhu Kiran
8	CITAES2018_paper_41	EEE	Fractional Order Lowpass Filter based on Operational Transresistance Amplifier with Different Order Fractional Capacitors	Gagandeep Kaur, A.Q Ansari, M. S. Hashmi

TRACK 2 & 3				
30-11-2018 SESSION - 3B : POWER ELECTRONICS, CONTROL & AUTOMATION				
VENUE: EEE AC SEMINAR HALL		ROOM NO: M222	TIME: 3.45 PM - 4.45 PM	
S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_59	EEE	SIMULATION AND PROTOTYPE IMPLEMENTATION OF HYBRID H-BRIDGE MLI WITH MINIMUM SWITCHES	Dr. A.Ramesh
2	CITAES2018_paper_76	EEE	Effect of Penetration of Solar DGs on Transient Stability of Captive Power Generation Units	Ramachandra Murthy K. V. S. , Bhimaraju P. S. D., Ravindra K
3	CITAES2018_paper_83	EEE	Efficient Power sharing of a Bi-Directional Converter Using Droop Controllers in an AC/DC Hybrid grid.	Y.V.Prashant, Dr.M.Siva kumar
4	CITAES2018_paper_86	EEE	Design and analysis of closed loop control of Multilevel SVPWM inverter fed PMSM Drive	Lavanya Komma, Dr. Alice Mary Karlapudy, Saraswathi G
5	CITAES2018_paper_87	EEE	Constraint based Design of Multipole Field Electromagnetic Launcher using Gauss Method	K.Sri Chandan, P. Mallikarjuna Rao
6	CITAES2018_paper_408	EEE	ANN Controlled Cascaded SEPIC - QRC Fed DC Drive System with Fast Dynamic Response	S. Vijaya Kumar, S. Siva Nagaraju
7	CITAES2018_PAPER_206	EEE	OPTIMAL CONDUCTOR SELECTION AND CAPACITOR PLACEMENT FOR COST MINIMIZATION IN DISTRIBUTION SYSTEMS	Dr. Kollu Ravindra, KRKV Prasad

TRACK 4 & 5				
01-12-2018 SESSION - 1C : SIGNAL PROCESSING & COMMUNICATIONS				
VENUE: MICROSOFT INNOVATION CENTER ROOM NO: M307 TIME: 10.30 AM - 11.30 AM				
S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_73	ECE	Integration of Compressive Sensing and Clustering in Wireless Sensor Networks using Block Triadiagonal Matrix Approach	P. Venkat Rao, Dr. Ch. Balaswamy
2	CITAES2018_paper_107	CSE	AUTHENTICATION AND INTEGRITY OF BIOMETRICS BY USING AUTHENTICATED ENCRYPTION.	P.Ramya, P.Naga Sravanthi, M.Mahesh Kumar
3	CITAES2018_paper_114	ECE	UWB antenna with Dual band notch characteristics at WiMAX/WLAN using CSRR	J.S.DEEPIKA, B.V.K.PADMAJA
4	CITAES2018_paper_404	ECE	Training of CC4 with neural network with spread unitary coding	pushpa sree potluri
5	CITAES2018_paper_405	ECE	Error correction capacity of unitary coding	pushpa sree potluri
6	CITAES2018_paper_411	ECE	PROVED CLASSIFICATION OF FETAL ABNORMALITIES USING AUTOMATED ABC ANFIS CLASSIFIER	NAGU MALOTHU, Dr.VVKDV PRASAD

TRACK 4 & 5				
01-12-2018 SESSION - 1D : SIGNAL PROCESSING & COMMUNICATIONS				
VENUE: MICROSOFT INNOVATION CENTER ROOM NO: M307 TIME: 11.45 AM - 01.00 PM				
S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_64	ECE	Investigation on compound reconfigurable solid state planar plasma antenna	Ranjith Perumal, Nakkeeran Rangaswamy, Srinivasarao Alluri
2	CITAES2018_paper_412	ECE	Implementation of Koch Snowflake Fractal Antenna for Multi-Band Applications	Dr.D.Prabhakar, E. Vargil Vijay, Dr. M.Satyanarayana, P. Ravi shankar
3	CITAES2018_paper_413	ECE	SPECKLE NOISE REDUCTION IN SAR IMAGES USING AMOSDS OPTIMIZATION WITH MULTIDIMENSIONAL TRANSFORMS	B.Vamsi Krishna, Dr.D.Ravi Krishna Reddy, G.Siva Mallika, A.RAVI
4	CITAES2018_paper_415	ECE	Design of practical minkowski and koch curved fractal antenna for Cognitive Radio applications	K.Vijayachandra, M.Sathyanarayana, Dr.B.T. Krishna
5	CIRAES2018_paper_422	ECE	Noise Estimation using Kalman Filter in Image Restoration	Nagasirisha.B, Dr. V.V.K.D.V. PRASAD
6	CITAES2018_paper_428	ECE	A Study on Inset-Fed Patch Antenna for High Frequency applications	P. Santosh, Prof. P.Mallikarjuna Rao
7	CITAES2018_paper_63	ECE	IoT Integrated Underwater WSNs – An Adaptive Routing for Balanced Energy Consumption	Veerapandu Geesala, kiranann vennuti
8	CITAES2018_paper_98	ECE	A Deep Learning network for ECG Heartbeat Arrhythmia Detection in telemedicine	Jagadeeswara Rao Annam, Suresh Babu Ch. Narayana Satyala, K. Srinivas

TRACK 4 & 5				
01-12-2018 SESSION - 1E : SIGNAL PROCESSING & COMMUNICATIONS				
VENUE: MICROSOFT INNOVATION CENTER ROOM NO: M307 TIME: 02.00 PM - 03.30 PM				
S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_40	ECE	An Intellectual Brain MRI Image Retrieval System to aid the diagnosis of Brain Tumors	Chethan K, Rekha Bhandarkar
2	CITAES2018_paper_46	ECE	Databases, Features, Classifiers and Challenges in Automatic Speech Recognition: A Review	Mahadevaswamy, D J Ravi
3	CITAES2018_paper_89	ECE	A Wideband Enhanced Circularly Polarized Patch Antenna with Modified Ground Plane for Wireless Applications	Dr.G.Anjaneyulu, J. Siddartha Varma,
4	CITAES2018_paper_424	ECE	Stockwell Transform Based Speaker Recognition Using Neural Network Classifier	Deepthi Mahankali, Dr. Friedhelm Schwenker
5	CITAES2018_paper_426	ECE	Impact on Gain and Noise : Novel Modelling Approach for OTA based ECG Amplifiers:	Rohith Bala Jaswanth B, Dr. V.V.K.D.V.Prasad, Dr. M.Kamaraju
6	CITAES2018_paper_430	ECE	Design and Performance Characteristics of Irregular Microstrip Patch Antenna Arrays	A.Venkata Satya Siva Prasad, Dr.P.A.NageswaraRao, B. Ashok Kumar, Prof.P.Mallikarjuna Rao
7	CITAES2018_PAPER_432	ECE	Histogram Of oriented Gradients with Kalman Filter approach for object Detection and Tracking	Madhavi Gajula, Dr. A. Jhansi Rani
8	CITAES2018_PAPER_433	ECE	Design, Simulation and Experimental validation of Wideband DGS Circularly Polarized Micro strip patch antenna for C-Band Satellite Communication Applications	Chitambara Rao Karedla, Mallikarjuna Rao Prudhivi

TRACK 6 & 7				
01-12-2018		SESSION - 2C : VLSI-ES & SOFTWARE ENGINEING		
VENUE: DECS LAB		ROOM NO: M321A	TIME: 10.30 AM - 11.30 AM	
S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_27	ECE	Design and Analysis of Multiplexer Based 4-Bit Flash ADC	E.Vijaya Babu, Dr. Y.Syamala, K. Sri Lakshmi, K. Siva Anjaneyulu
2	CITAES2018_paper_28	ECE	Development of Low-Power VLSI Circuits for Biomedical Applications	K. Srilakshmi , Dr. A. V. N Tilak, Dr. Y. Syamala
3	CITAES2018_paper_39	ECE	Design and Implementation of 6-Stage 64-bit MIPS Pipelined Architecture	P. Indira, Dr. M. Kamaraju
4	CITAES2018_paper_72	ECE	DESIGN OF FREQUENCY EFFICIENT CMOS DIFFERENCE AMPLIFIER CIRCUIT FOR SIGMA DELTA ADC FOR AEROSPACE APPLICATIONS	Suneetha Telidevulapalli, Srinivasa Rao Talla
5	CITAES2018_paper_406	ECE/CSE	Implementation of Tunnel Field Effect Transistor in Cadence Model for the Logic Design	Venkata Harshavardhan Nagalla, Vinay chitturi
6	CITAES2018_paper_407	ECE	Cloud Robotics and Automation	Venkat Chowdary Medasani, Anil Kumar Burra

TRACK 6 & 7				
01-12-2018		SESSION - 2D : VLSI-ES & SOFTWARE ENGINEERING		
VENUE: DECS LAB		ROOM NO: M321A	TIME: 11.45 AM - 01.00 PM	
S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_52	ECE/IT	Automatic Water Level Controller Using Aurdino	Ch.SugunaLatha, T. Sumallika Tedla Balaji
2	CITAES2018_paper_79	CSE	A Rough Set approach for Knowledge Acquisition on Vector born diseases	G K Panda, Indrani Sahu, Susant Kumar Das
3	CITAES2018_paper_95	ECE	Design and Implementation of Linear Feedback Shift Register in Subthreshold Region	B Lakshmi, Dr. M Kamaraju, Dr. K Babulu,
4	CITAES2018_paper_99	ECE	Tracking Industrial Assets using Blockchain	K Rajeev Brahma
5	CITAES2018_paper_100	ECE	ON BOARD DIGITAL BEAM FORMING FOR RADAR COMMUNICATION USING VERTEX FPGA	Dr.P.A.Harsha Vardhini, N.Koteswaramma, V.Prakasam
6	CITAES2018_paper_102	ECE	IMPLEMENTATION OF DATALOGGER FOR LIVE TRACKING OF VEHICLE PARAMETERS USING GSM-GPS FOR SECURITY	K.Radhika, Y.Murali Mohan Babu, D. Nagajyothi
7	CITAES2018_paper_414	CSE	Predicting plausible tax defaulters using decision trees	Priya Mehta, Jithin Mathews, K. Suryamukhi, Dr. Ch. Sobhan Babu
8	CITAES2018_paper_431	ECE	A SURVEY OF VLSI ARCHITECURES FOR HAMMING CODE ALGORITHM	S. Aparna, Y. Sri Chakrapani, T. Venkata Lakshmi, Dr. M. Kamaraju

TRACK 6 & 7

01-12-2018

SESSION - 2E : VLSI-ES & SOFTWARE ENGINEERING

VENUE: DECS LAB

ROOM NO: M321A

TIME: 02.00 PM - 03.30 PM

S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_105	CSE	SURVEY ON DATA CLASSIFICATION TECHNIQUES AND TOOLS OF BIG DATA	KUNCHAPARTHI JYOTHSNA LATHA, MARKAPUDI BABURAO, CHADUVULA KAVITHA
2	CITAES2018_paper_108	CSE	Application of Rough Set Theory in Medical Data Analytics	Indrani Sahoo
3	CITAES2018_paper_112	ECE	LOW POWER SOC DESIGN USING ZYNQ-7000 FPGA BOARD USING VIVADO-TOOL	T. Anil Chowdary, Dr. M. Durga Prakash
4	CITAES2018_paper_401	ECE	Active Pixel Sensor Using CNTFET	Venkata Harshavardhan Nagalla, Vinay chitturi
5	CITAES2018_paper_417	ECE	A NOVEL ADAPTIVE PREDICTION TECHNIQUE ROUTING ALGORITHM IN NETWORK ON CHIP	M.V.Punnaiah, N.V.Apparao, A.Naga Ganesh
6	CITAES2018_paper_418	ECE	FPGA Based Dynamic Hardware Reconfigurable System using Partial Reconfiguration Procedure	Kalakurasa Rakesh, Moturi Satyanarayana
7	CITAES2018_paper_425	CSE	Evaluation of Television Shows Popularity based on Twitter Data using Sentiment Analysis Techniques	Dr. D. N. V. S. L. S. Indira, Dr. G. V. S. N. R.V. Prasad, Mr. JNVR Swarup Kumar
8	CITAES2018_paper_429	ECE	Application of multi sensors for tracking a maneuvering object	M. Damodhar Rao, G. Naga Divya, V.V.K.D.V. Prasad

TRACK 1				
01-12-2018 SESSION - 4A : POWER SYSTEMS				
VENUE: EEE AC SEMINAR HALL ROOM NO: M222 TIME: 10.00 AM - 11.30 AM				
S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_21	EEE	Cost Management of a CFBC Boiler in a Thermal Power Plant	P V Narendra Kumar, Dr Ch.Chengaiyah, G Kiran Kumar
2	CITAES2018_paper_23	EEE	Heart Rate Monitoring using Arduino and AD8232 Board	Puranam Revanth Kumar, Yannam Srinija Reddy, Resmi R
3	CITAES2018_paper_45	EEE	Implementation of Adaptive PSODV Hybrid Algorithm to Improved Benders Decomposition based Unit Commitment problem	Dr.L. Ravi Srinivas, M.Ramu, S.TaraKalyani
4	CITAES2018_paper_50	EEE	Demand Side Management in Smart Grid using Internet of Things	Suresh Makkena, P. Balakoteswara Rao, Anil Kumar Yarlagadda
5	CITAES2018_paper_69	EEE	Classification of faults in three phase transmission line using euclidean distance technique	Vishnu venkata sri sai Vaddi, Manohar Kintali
6	CITAES2018_paper_78	EEE	Hybrid Differential EvolutionBased Voltage and Frequency control of DG Unit in an Island Mode Microgrid.	Bhukya.Mothi Ram, Dr.L.Ravi Srinivas,
7	CITAES2018_paper_84	EEE	L-Index based power loss minimization using SVC under network contingency	Y.V.R. Mounika, B. Sravana kumar

TRACK 1				
01-12-2018 SESSION - 4B : POWER SYSTEMS				
VENUE: EEE AC SEMINAR HALL ROOM NO: M222 TIME: 11.45 AM - 01.00 PM				
S. NO.	PAPER ID	DEPARTMENT	TITLE OF THE PAPER	AUTHORS
1	CITAES2018_paper_88	EEE	POWER SYSTEM VOLTAGE STABILITY ANALYSIS BY CONVENTIONAL METHODS	Madhuranthaka, Gowri Manohar
2	CITAES2018_paper_90	EEE	SAPF For Power Quality Improvement Based On PSONE Optimization Algorithm	B.Mahesh Babu, Dr. L.Ravi Srinivas Dr. S.S.Tulasi Ram
3	CITAES2018_paper_94	EEE	Clonal Selection Algorithm for Reactive Power Optimization with Voltage Stability Enhancement	P.L.V.Prasanna , K.Aravind Kumar, Sarat Kumar Sahu
4	CITAES2018_paper_97	EEE	Identification of the best location and size of IPFC to optimize the cost and to improve the power system security using Firefly optimization algorithm	A Amarendra, Dr. L Ravi Srinivas, Dr. R Srinivasa Rao
5	CITAES2018_paper_110	EEE	FRACTIONAL ORDER PID CONTROLLED UNIFIED POWER QUALITY CONDITIONER SYSTEM WITH ENHANCED RESPONSE	Sk.Abdul pasha, N.Prema kumar
6	CITAES2018_paper_204	EEE	Maximum power point tracking scheme of photo voltaic array using an evolutionary algorithm	K.Ravindra, B.S.Varunsai